



Product Information

DATE: 30.Apr.2007

SAMSUNG TFT-LCD

MODEL: LTI320WT-L07

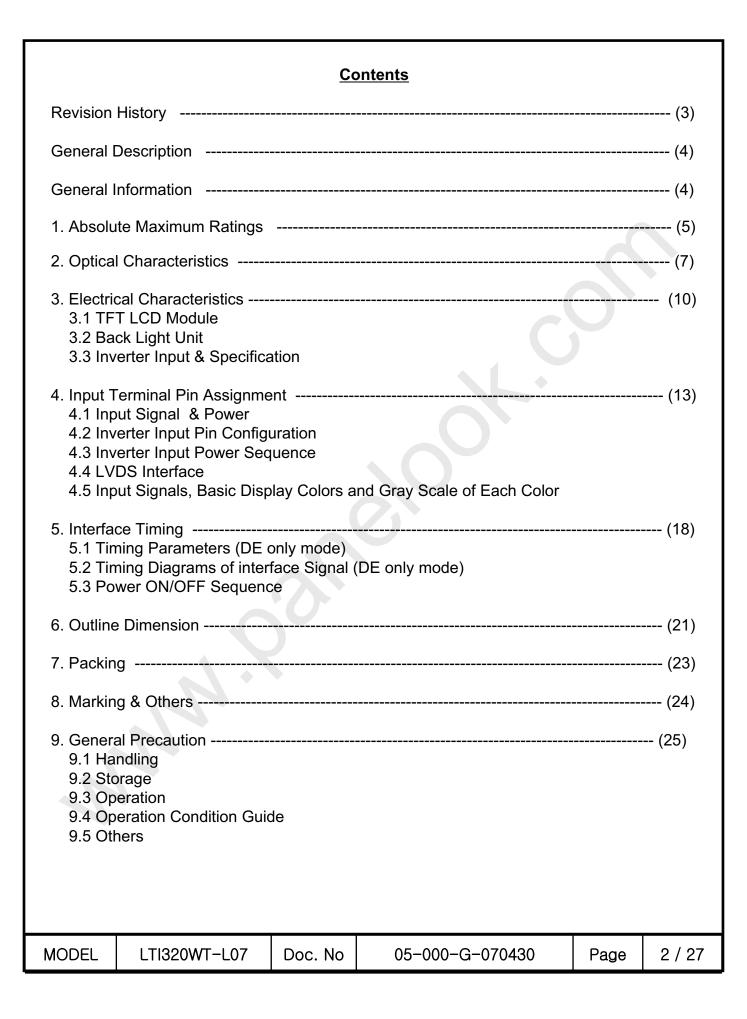
The Information Described in this Specification is Preliminary and can be changed without prior notice

APPROVED BY	DATE	PREPARED BY	DATE
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Development Team 2, LCD Business

Samsung Electronics Co., LTD.

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* Revision History

Date	Rev. No	Page	Summary
Apr 30, 2007	000	all	First issued

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General Description

Description

LTI320WT-L07 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 32.0" is 1366 x 768 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and High Definition TV (HDTV).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, High luminance
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- WXGA (1366 x 768 pixels) resolution (16:9)
- Low Power consumption
- Direct Type 12 CCFTs(Cold Cathode Fluorescent Tube)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

General Information

Items	Specification	Unit	Note
ILGIIIS	Specification	Offic	NOLE
Module Size	$760.0(W_{TYP}) \times 450.0(H_{TYP})$	mm	±1.0mm
Woddie Oize	48.0(D _{MAX})	111111	
Weight	7,500(Max.)	g	
Pixel Pitch	0.51075(H) x 0.51075(V)	mm	
Active Display Area	697.6845(H) x 392.256(V)	mm	
Surface Treatment	Haze 44% , Hard-coating (3H)		
Display Colors	8 bit - 16.7M	colors	
Number of Pixels	1366 x 768	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	600 (Тур.)	cd/m ²	

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V _{DD}	GND-0.5	5.5	V	(1)
Storage temperature		T _{STG}	-20	60	Ĉ	(2)
Glass surface	Center	T _{OPR}	0	50	°C	(0) (5)
temperature (Operation)	T. Uniformity	ΔT	-	10	c	(2),(5)
Shock (non - operating)		S _{nop}	-	50	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) Ta= 25 \pm 2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
 - a. 90 % RH Max. (Ta \leq 39 °C)
 - b. Relative Humidity is 90% or less. (Ta > 39 °C)
 - c. No condensation
- (3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

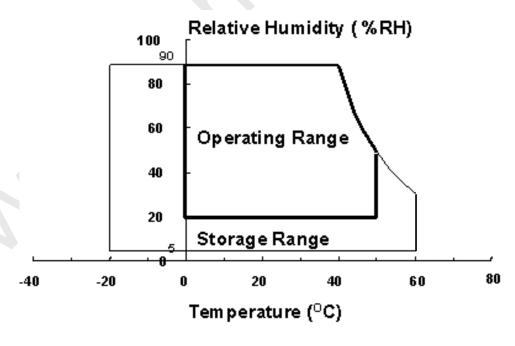
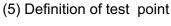
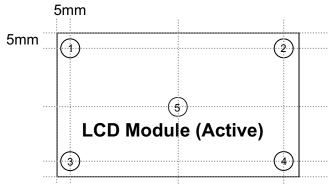


Fig. Temperature and Relative humidity range

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 T_{OPR} : Temperature of the center of the glass surface (Test point 5) T1 ~ T4 : Temperature of each edge of the glass surface

 T_{MAX} : The highest temperature of the glass surface

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2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON BM-7,SPECTRORADIOMETER SR-3

(Ta = 25
$$\pm$$
 2°C, VDD = 5V, fv = 60Hz, $\rm f_{DCLK}$ = 75MHz, $\rm I_{L}$ = 8.0mArms)

Item		Symbol	Condition	Min.	Tun	Max.	Unit	Note
Contrast I		C/R	Condition	1500	Typ. 2000	IVIAX.	Offic	(3)
(Center of s	creen)			1000				SR-3
D	Rising	Tr		-	10	13		(5)
Response Time	Falling	Tf		-	6	10	msec	(5) BM-7
	G-to-G	Tg		-	8	-		Din 1
Luminance of (Center of s		Y _L	Normal θ L,R =0	550	600	- (cd/m ²	(6) SR-3
		Rx	$\theta \mathbf{U}, \mathbf{D} = 0$		0.635			
	Red	Ry	Viewing		0.326	\		
Color Chromaticity (CIE 1931)	0	Gx	Angle		0.279			
	Green	Gy		TYP.	0.591	TYP.		(7),(8)
	Dive	Вх		-0.03	0.144	+0.03		SR-3
	Blue	Ву			0.061			
	1A // '/	Wx			0.280			
	White	Wy			0.290			
Color Ga	mut	-		-	72	-	%	(7) SR-3
Color Temp	erature	-		-	10,000	-	К	(7) SR-3
		θ_{L}		75	89	-		
Viewing Angle	Hor.	θ_{R}	0/5> 40	75	89	-	<u> </u>	(8)
	1/	θυ	C/R≥10	75	89	-	Degree	SR-3
	Ver.	θ_{D}		75	89	-	1	
Brightness U		B _{uni}		-	-	25	%	(4) SR-3

Note (1) Test Equipment Setup

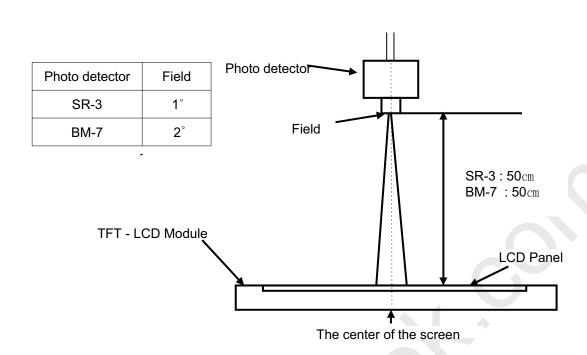
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Single lamp current: 8.0mA

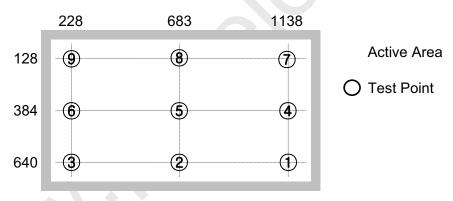
Environment condition : Ta = 25 \pm 2 °C

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Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

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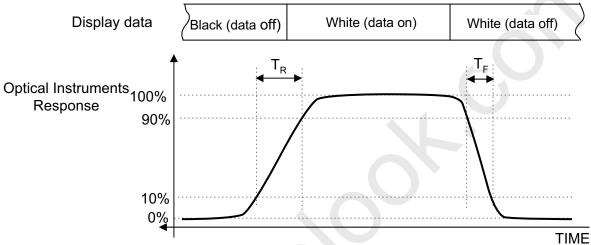


Note (4) Definition of 9 points brightness uniformity

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness Bmin : Minimum brightness

Note (5) Definition of Response time : Sum of Tr, Tf

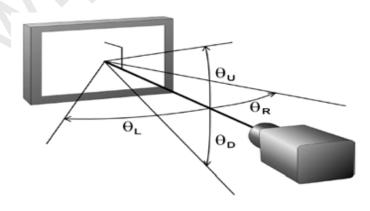


Note (6) Definition of Luminance of White: Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle : Viewing angle range (C/R ≥ 10)



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4. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25° C \pm 2 $^{\circ}$ C

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of	Power Supply	V _{DD}	4.5	5.0	5.5	V	(1)
Current	(a) Black		-	1000	-	mA	
of Power Supply	(b) White	I _{DD}	-	1400	-	mA	(2),(3)
	(c) N-Pattern		-	1500	2200	mA	
Vsync Free	quency	f _V	48	60	66	Hz	
Hsync Fre	quency	f _H	44	48	53	kHz	
Main Frequ	uency	f _{DCLK}	65	75	82	MHz	
Rush Curr	ent	I _{RUSH}	-	-	4	Α	(4)

Note (1) The ripple voltage should be controlled under 10% of $V_{\rm DD}$.

- (2) fv = 60Hz, fDCLK = 75MHz, V_{DD} = 5.0V, DC Current.
- (3) Power dissipation check pattern (LCD Module only)

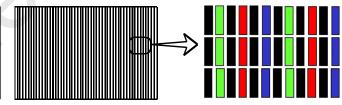




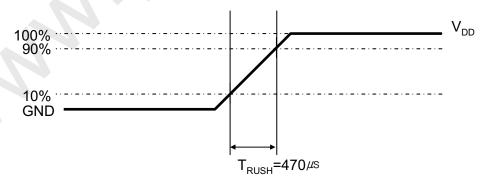








(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when $\ T_{RUSH}.$ is $470\,\mu\text{s}.$

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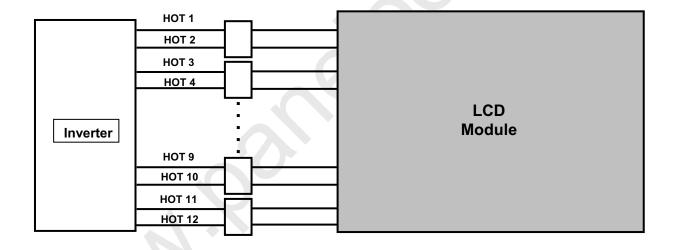
3.2 Back Light Unit

The back light unit contains 12 direct-lighting type CCFTs (Cold Cathode Fluorescent Tube). The characteristics of lamps are shown in the following tables.

Ta=25 \pm 2°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp Current	IL	3.0	8.0	8.5	mArms	
Lamp Voltage	V _L	-	1200	-	Vrms	
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value. [Operating condition : $Ta = 25 \pm 2^{\circ}C$, IL = 8.0mArms, For single lamp only]



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3.3 Inverter Input Condition & Specification

Global LCD Panel Exchange Center

Items	Symbol	Conditions	Sp	pecificatio	ns	Unit	Note	
nems	Symbol	Conditions	Min.	Min. Typ.		Offile	Note	
Input Voltage	Vin	-	22	24	26	V	Ta=25 ±2 °C	
Input Current	lin	Vin=24.0V Vdim=3.3V	-	1	4.1	А		
Lamp Current	I _{O,MAX}	Vdim=3.3V	7.5	8.0	8.5	mArms	After 1 hour Warm-up	
Frequency	F_LAMP	Vin=24.0V Vdim=3.3V	60.0	62.5	65.0	kHz		
Backlight	ON	Vin=24.0V	2.4	-	5.25	V		
On/Off	OFF	VIN=24.UV	0	-	0.8	V	-	
Dimming	V	Max Lum	3.3		<u> </u>	V		
Control	V_{DIM}	Min. Lum	-	(-)	0	V	-	

Note (1) Power Consumption is measured at 600[cd/m2] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.

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Connector: FI-E30S (JAE)



4. Input Terminal Pin Assignment

4.1 Input Signal & Power

PIN No.	Description	PIN No.	Description
1	No Connection (Note 1)	16	GND
2	No Connection (Note 1)	17	RxIN3-
3	No Connection (Note 1)	18	RxIN3+
4	GND	19	GND
5	RxIN0-	20	GND
6	RxIN0+	21	LVDS Option (Note 2)
7	GND	22	No Connection (Note 1)
8	RxIN1-	23	GND
9	RxIN1+	24	GND
10	GND	25	GND
11	RxIN2-	26	Vdd (5V)
12	RxIN2+	27	Vdd (5V)
13	GND	28	Vdd (5V)
14	RxCLK-	29	Vdd (5V)
15	RxCLK+	30	Vdd (5V)

Note (1) No Connection : These pins are only used for SAMSUNG internal purpose.

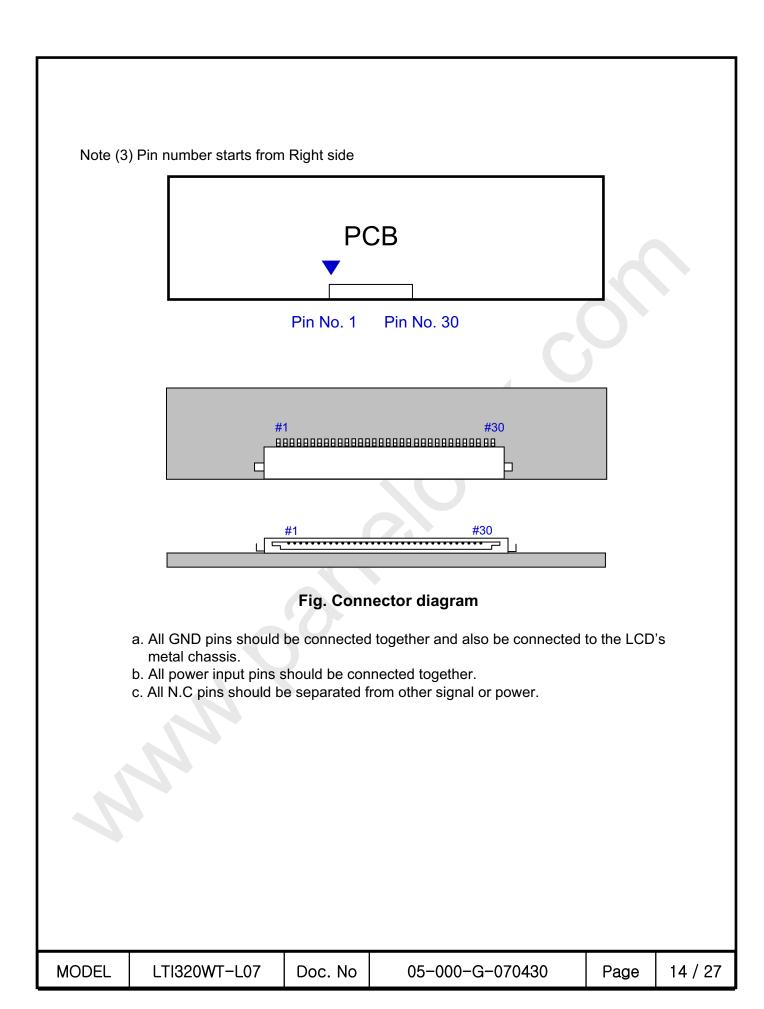
(2) LVDS Option : High (3.3 V) or Open (N.C) → Normal LVDS format

: Low (GND) \rightarrow JEIDA LVDS format

Sequence :On = $V_{DD}(T1) \ge LVDS$ Option $\ge Interface$ Signal(T2)

Off = Interface Signal(T3) ≥ LVDS Option ≥ VDD

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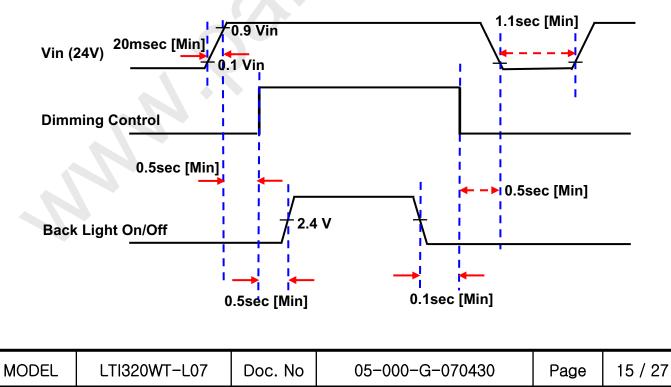


4.2 Inverter Input Pin Configuration

Connector : JST, S14B-PHA-SM-TB(LF)

Pin No.	Pin Configuration(FUNCTION)
1	Vin (24V)
2	Vin (24V)
3	Vin (24V)
4	Vin (24V)
5	Vin (24V)
6	GND
7	GND
8	GND
9	GND
10	GND
11	No Connection
12	Backlight On /Off [On: 2.4 ~ 5.25 V, Off: 0 ~ 0.8 V]
13	Dimming Control [0V: Min, 3.3V: Max]
14	No Connection

4.3 Inverter Input Power Sequence





4.4 LVDS Interface

- LVDS Receiver : Tcon (merged)

- Data Format (JEIDA & Normal)

Default LVDS Option : VESA

		LVDS pin		JEIDA -DATA	VESA -DATA		
		TxIN/RxOU	ГО	R2	R0		
		TxIN/RxOU	Γ1	R3	R1		
		TxIN/RxOU	Γ2	R4	R2		
Tx	OUT/RxIN0	TxIN/RxOU	Г3	R5	R3		
		TxIN/RxOU	Г4	R6	R4		
		TxIN/RxOU	Г6	R7	R5		
		TxIN/RxOU	Г7	G2	G0		
		TxIN/RxOU	Г8	G3	G1		
		TxIN/RxOU	Г9	G4	G2		
		TxIN/RxOUT	12	G5	G3		
Tx	OUT/RxIN1	TxIN/RxOUT	13	G6	G4		
		TxIN/RxOUT	·14	G7	G5		
		TxIN/RxOUT	15	B2	В0		
		TxIN/RxOUT	18	B3	B1		
		TxIN/RxOUT	·19	B4	B2		
		TxIN/RxOUT	20	B5	В3		
		TxIN/RxOUT	21	B6	B4		
Tx	OUT/RxIN2	TxIN/RxOUT	22	B7	B5		
		TxIN/RxOUT	24	HSYNC	HSYNC		
		TxIN/RxOUT	25	VSYNC	VSYNC		
		TxIN/RxOUT	26	DEN	DEN		
		TxIN/RxOUT	27	R0	R6		
		TxIN/RxOU	Г5	R1	R7		
	3	TxIN/RxOUT	10	G0	G6		
Tx	OUT/RxIN3	TxIN/RxOUT	11	G1	G7		
		TxIN/RxOUT	16	В0	В6		
		TxIN/RxOUT	17	B1	B7		
		TxIN/RxOUT	23	RESERVED	RESERVED		
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4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												D/	ATA S	SIGN	٩L											GRAY
COLOR	DISPLAY (8bit)				RE	ΞD							GRI	EEN							BL	UE				SCALE
	,	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	В1	B2	ВЗ	В4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	↑ 	:	:	:	:	:	:			:	:	:	:	:	:				:	:	:	:	:			R3~
OF RED	↓	:	:	:	:	:	:			:	:	:	:	:	: (:	:	:	:	:	:			R252
LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
ODAY	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE	↑	:	:	:	:	:	<u> </u> :				:	:	:	:	:			:	:	:	:	:	:			G3~
OF GREEN	↓	:	:	:	:	:	:				:	:	:	:	:			:	:	:	:	:	:			G252
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
CDAY	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
GRAY SCALE	1)	():	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE	1	:		:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B252
1	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
	133	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray:

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

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5. Interface Timing

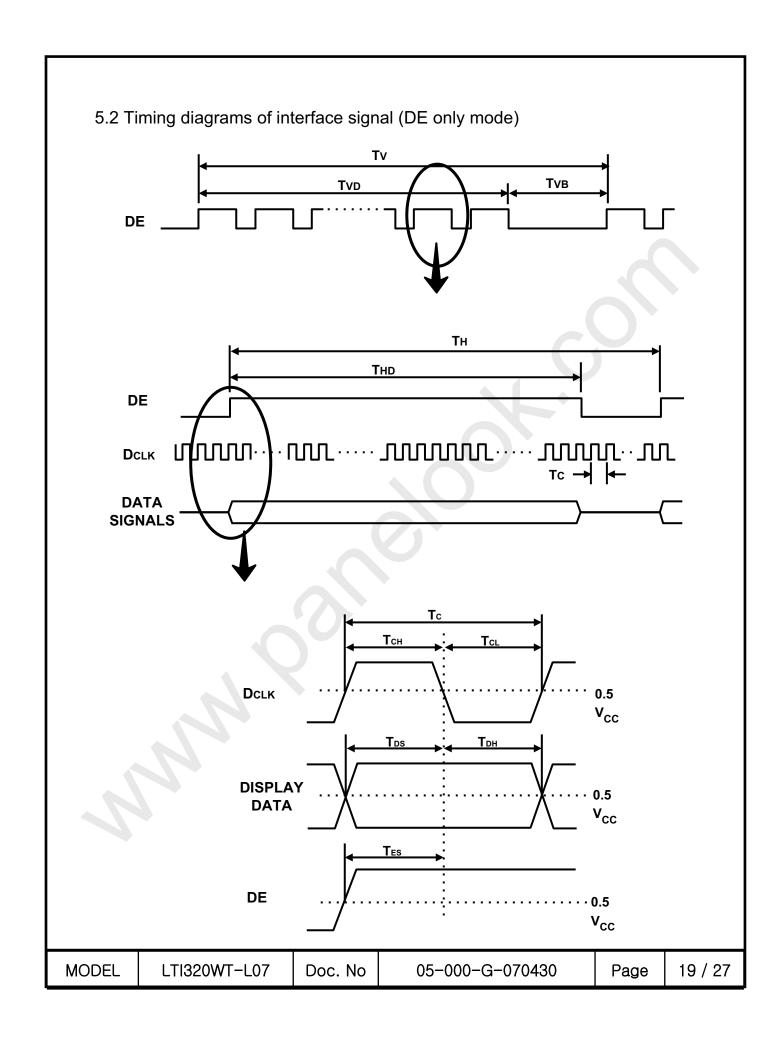
5.1 Timing Parameters (DE only mode)

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock		1/T _C	65	75	82	MHz	-
Hsync	Frequency	F _H	44	48	53	KHz	-
Vsync		F _V	48	60	66	Hz	-
Vertical Display Term	Active Display Period	T _{VD}	-	768	-	Lines	-
	Vertical Total	T _V	773	838	1200	Lines	-
Horizontal Display Term	Active Display Period	T _{HD}	-	1366	-	Clocks	-
	Horizontal Total	T _H	1460	1600	2000	Clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

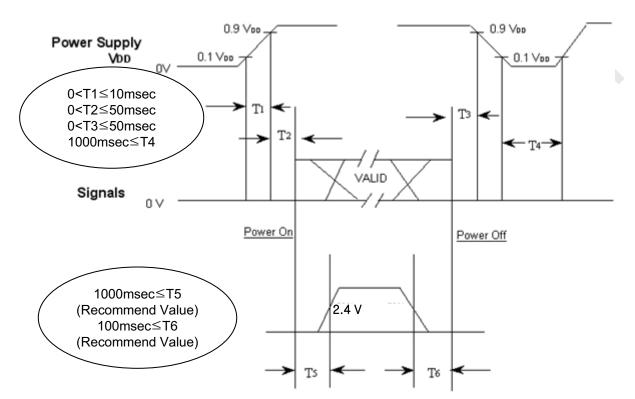
Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

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5.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1: V_{DD} rising time from 10% to 90%

T2 : The time from V_{DD} to valid data at power ON.

T3 : The time from valid data off to $V_{\rm DD}$ off at power Off.

T4: V_{DD} off time for Windows restart

T5: The time from valid data to B/L enable at power ON.

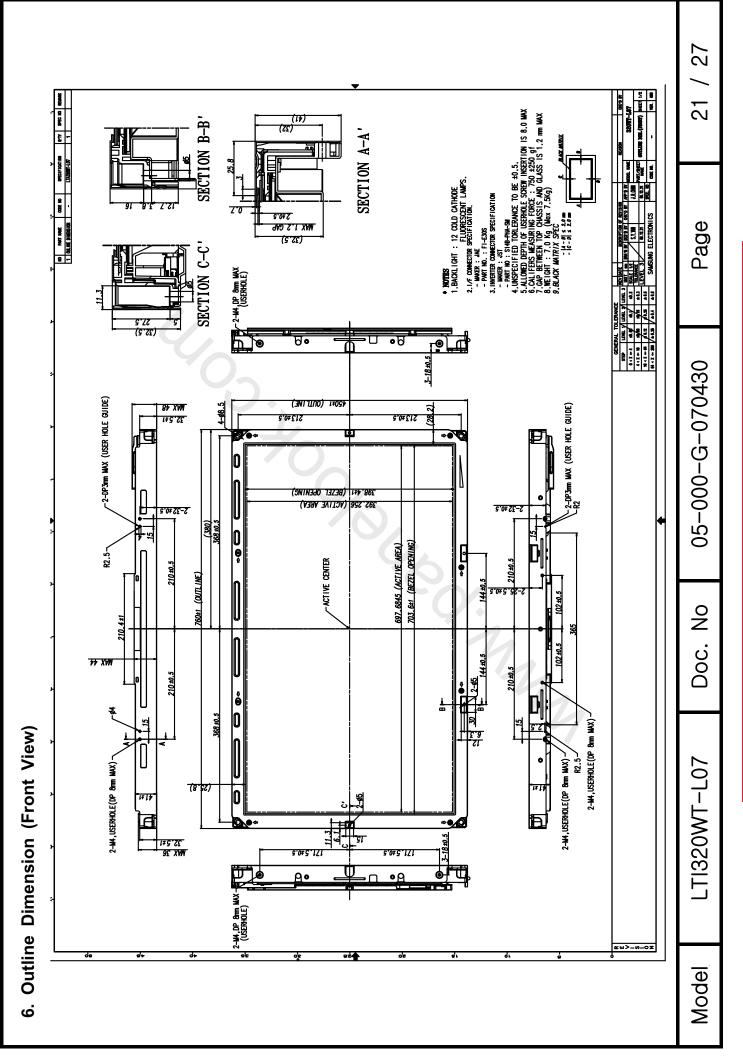
T6: The time from valid data off to B/L disable at power Off.

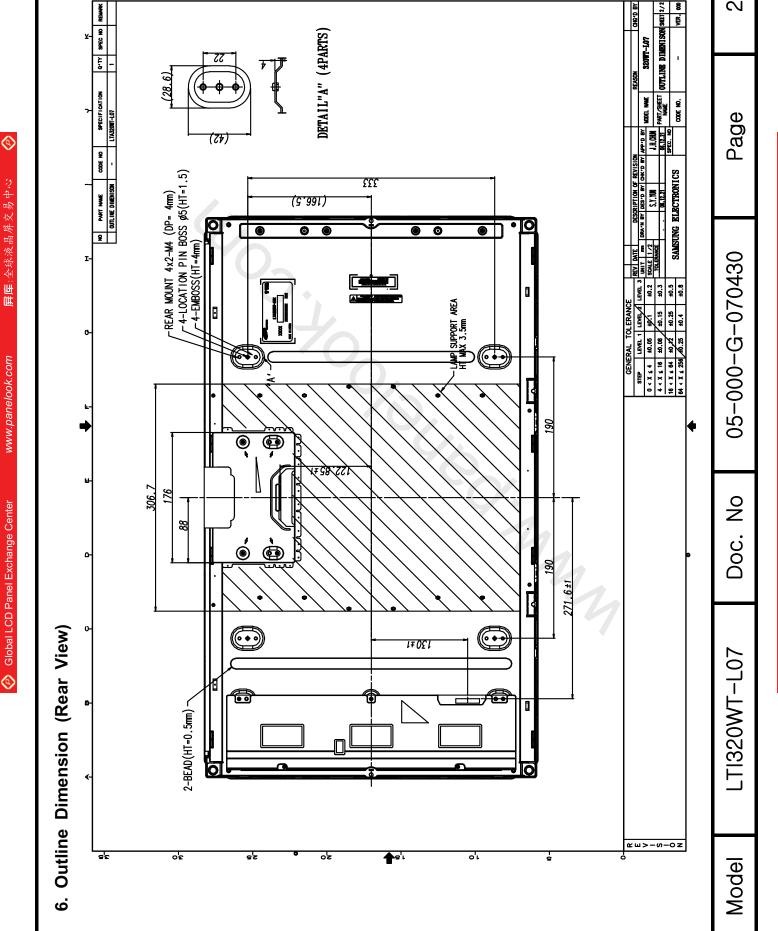
- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level,
 please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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(1)

Global LCD Panel Exchange Center





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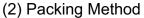
VER. 000

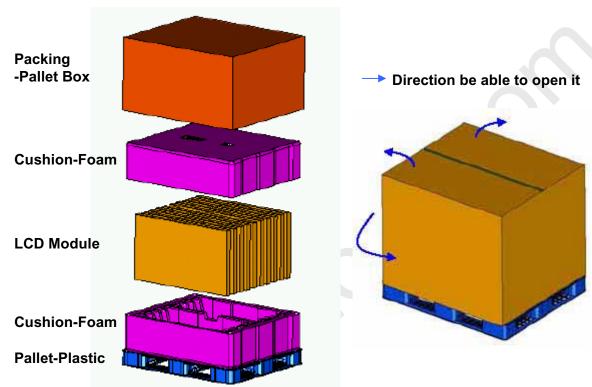
CHG*D BY



7. PACKING

- 7.1 CARTON (Internal Package)
- (1) Packing Form
 Corrugated fiberboard box and corrugated cardboard as shock absorber





7.2 Packing Specification

Item	Specification	Remark
LCD Packing	12ea / (Packing- Pallet Box)	1. 84Kg / LCD (12ea) 2. 7 Kg / Cushion-pallet (2ea) 3. 5.7 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8.0kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 125mm(height)
Total Pallet Weight	104.7 kg	Pallet(8kg) + Module (7.0*12=84kg) + Cushion (up + bottom=7kg) + Pallet-BOX(8.8kg)

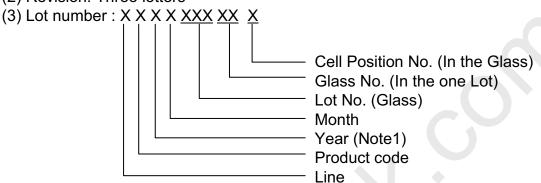
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8. MARKING & OTHERS

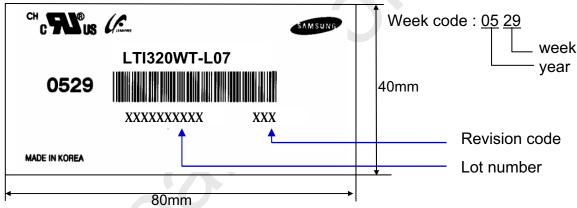
A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number : LTI320WT-L07

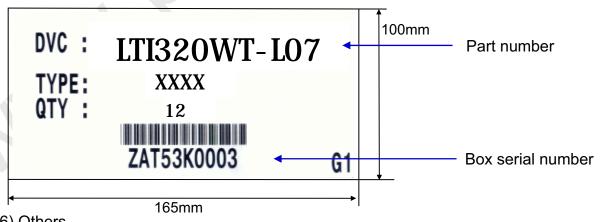
(2) Revision: Three letters



(4) Nameplate Indication



(5) Packing box attach



(6) Others

 After service part Lamps cannot be replaced because of the narrow bezel structure.

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9. General Precautions

- 9.1 Handling
- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.

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9.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to $35\,^{\circ}$ C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

9.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

9.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.Normal condition is defined as below;

- Temperature : 20±15℃

- Humidity : $55\pm20\%$

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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9.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)

 Otherwise the Module may be damaged.
 - Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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